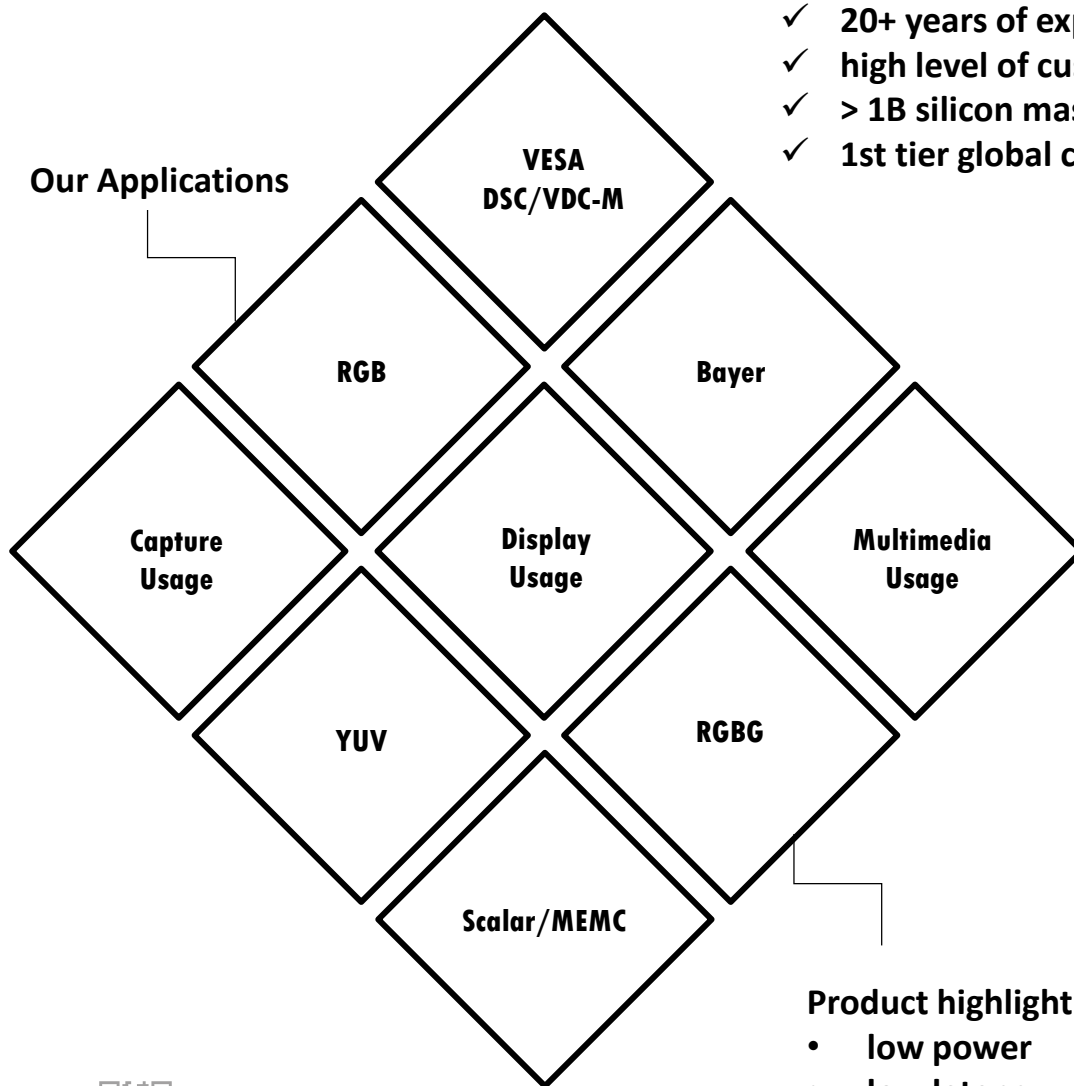


# TITC

Image Compression IP specialist

- ✓ 20+ years of experience
- ✓ high level of customization
- ✓ > 1B silicon mass produced
- ✓ 1st tier global customers

Our Applications



Product highlight features:

- low power
- low latency
- small area



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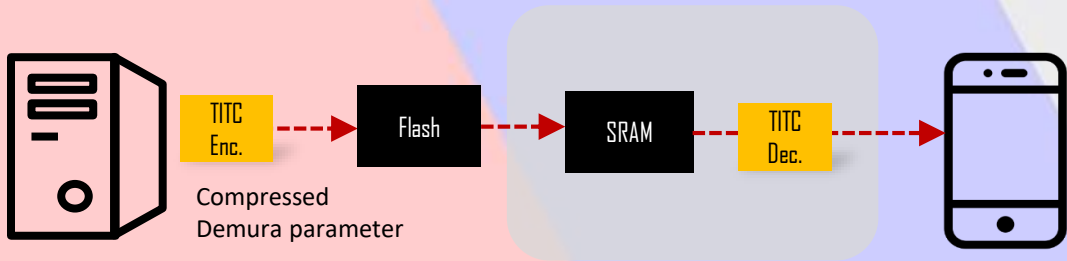
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# TITC D-Series IP

## Demura parameter for Flash

TITC proprietary De-Mura Compression IP is comprised of the Software Encoder and the Hardware Decoder. The compression IP can be configured according to different parameters such as Flash size (e.g. compressed data size is configurable from 16Mbits to 8Mbits), and bin-sizes (i.e. pixel downsample size like 2by2, 4by4, and others).

The software encoder can incorporate customer's De-Mura table format (downsampled or non-downsampled). We provide customized service to stitch customer's De-Mura data, and preprocessing with our data compression IP seamlessly. The hardware decoder can adapt to customer's requirement on throughputs. We provide multi instance architecture to meet high throughput needs. Furthermore, TITC proprietary Compression IP has already been validated by large OLED/LCD panel makers and licensed by IC Fabless customers.



### ➤ TITC DeMura IP

Usage / Series		display / D-series	
IP Name		Demura v1	Demura v2
Data	Type	RGB/RGBG (Demura parameter)	RGB/RGBG (Demura parameter)
	Bit-Depth	8-bit	8/10-bit
Compression	Type	Lossy	Lossy
	Ratio(Lossy)	3~3.8X	compr. as 16 or 8MB
	Unit	frame	frame
Performance	Throughput	4-pix/12-comp (per T)	8-comp (per T)
Note		* enc: software /dec: RTL	* enc: software /dec: RTL